



### ESDxxX6-3LCD20

SFP+

#### 14.025Gbps SFP+ DWDM Transceiver, Single Mode, 20km Reach

- Supports up to 14.025Gbps bit rates
- Hot-pluggable SFP+ footprint
- 100GHz ITU, C Band DWDM Cooled EML laser and PIN photodiode, Up to 20km for SMF transmission
- > Compliant with SFP+ MSA and SFF-8472 with duplex LC receptacle
- Compatible with RoHS
- Single +3.3V power supply
- Real Time Digital Diagnostic Monitoring
- Operating case temperature:
  Standard: 0 to +70°C



### **Applications**

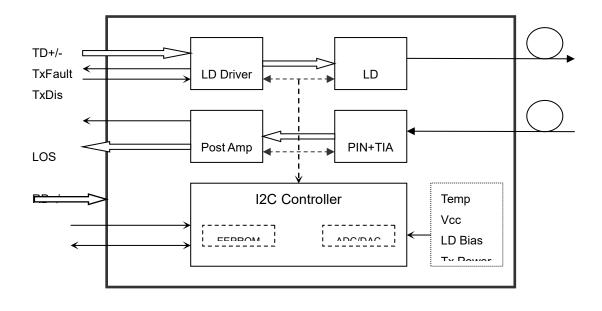
- ➤ 4.25/8.5/14.025G Fibre channel
- Other Optical links

### Description

The SFP+ transceivers are high performance, cost effective modules supporting data rate of 14.025Gbps and 20km transmission distance with SMF.

The transceiver consists of three sections: a Cooled EML laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The transceivers are compatible with SFP Multi-Source Agreement and SFF-8472 digital diagnostics functions.



#### Transceiver functional diagram

### **Absolute Maximum Ratings**

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

## Wavelength Guide Pin Descriptions

#### ESDxxX6-3LCD20

λC Wavelength Guide						
ITU Channel Product Code	Frequency(THz)	Wavelength	ITU Channel Product Code	Frequency(THz)	Wavelength	
17	191.7	1563.86	40	194.0	1545.32	
18	191.8	1563.05	41	194.1	1544.53	
19	191.9	1562.23	42	194.2	1543.73	
20	192.0	1561.42	43	194.3	1542.94	
21	192.1	1560.61	44	194.4	1542.14	
22	192.2	1559.79	45	194.5	1541.35	
23	192.3	1558.98	46	194.6	1540.56	
24	192.4	1558.17	47	194.7	1539.77	
25	192.5	1557.36	48	194.8	1538.98	
26	192.6	1556.55	49	194.9	1538.19	
27	192.7	1555.75	50	195.0	1537.40	
28	192.8	1554.94	51	195.1	1536.61	
29	192.9	1554.13	52	195.2	1535.82	
30	193.0	1553.33	53	195.3	1535.04	
31	193.1	1552.52	54	195.4	1534.25	
32	193.2	1551.72	55	195.5	1533.47	
33	193.3	1550.92	56	195.6	1532.68	
34	193.4	1550.12	57	195.7	1531.90	
35	193.5	1549.32	58	195.8	1531.12	
36	193.6	1548.51	59	195.9	1530.33	
37	193.7	1547.72	60	196.0	1529.55	
38	193.8	1546.92	61	196.1	1528.77	
39	193.9	1546.12	-	-	-	

## **Recommended Operating Conditions**

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Тс	0		+70	°C
Power Supply Voltage	Vcc	3.135	3.30	3.465	V
Power Supply Current	lcc			550	mA
Data Rate		4.25	14.025		Gbps

## **Optical and Electrical Characteristics**

Para	neter	Symbol	Min	Typical	Max	Unit	Notes
			Transmi	tter			
Centre V	Vavelength	λc	1528.77		1563.86	nm	
Spectral W	idth(-20dB)	Δλ			1	nm	
Side-Mode Su	ppression Ratio	SMSR	30	-		dB	
Average C	Output Power	P <sub>out</sub>	-1		+3	dBm	1
Extinct	ion Ratio	ER	8.2			dB	
Data Input Sv	wing Differential	V <sub>IN</sub>	180		850	mV	2
Input Differer	ntial Impedance	Z <sub>IN</sub>	90	100	110	Ω	
TX Disable	Disable		2.0		Vcc	V	
TX DISable	Enable		0		0.8	V	
	Fault		2.0		Vcc	V	
TX Fault	Normal		0		0.8	V	
			Receiv	er			
Centre V	Centre Wavelength		1260		1620	nm	
Receiver	Sensitivity				-14	dBm	3
Receive	r Overload		0.5			dBm	3
LOSE	LOS De-Assert				-15	dBm	
LOS	LOS Assert		-28			dBm	
LOS H	LOS Hysteresis		0.5			dB	
Data Output S	Data Output Swing Differential		300		900	mV	4
		High	2.0		Vcc	V	
L	LOS				0.8	V	

#### Notes:

1. The optical power is launched into SMF.

2. PECL input, internally AC-coupled and terminated.

3. Measured with a PRBS 2<sup>31</sup>-1 test pattern @14025Mbps, BER  $\leq 1 \times 10^{-12}$ .

4. Internally AC-coupled.

### **Timing and Electrical**

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			2	ms
Tx Disable Assert Time	t_off			100	μs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μs
Tx Disable To Reset	t_reset	10			μs
LOS Assert Time	t_loss_on			100	μs
LOS De-assert Time	t_loss_off			100	μs
Serial ID Clock Rate	f_serial_clock		100	400	KHz
MOD_DEF (0:2)-High	V <sub>H</sub>	2		Vcc	V
MOD_DEF (0:2)-Low	VL			0.8	V

### Diagnostics

Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal
Voltage	3.0 to 3.6	V	±3%	Internal
Bias Current	0 to 100	mA	±10%	Internal
TX Power	-1 to +3	dBm	±3dB	Internal
RX Power	-16 to -1	dBm	±3dB	Internal

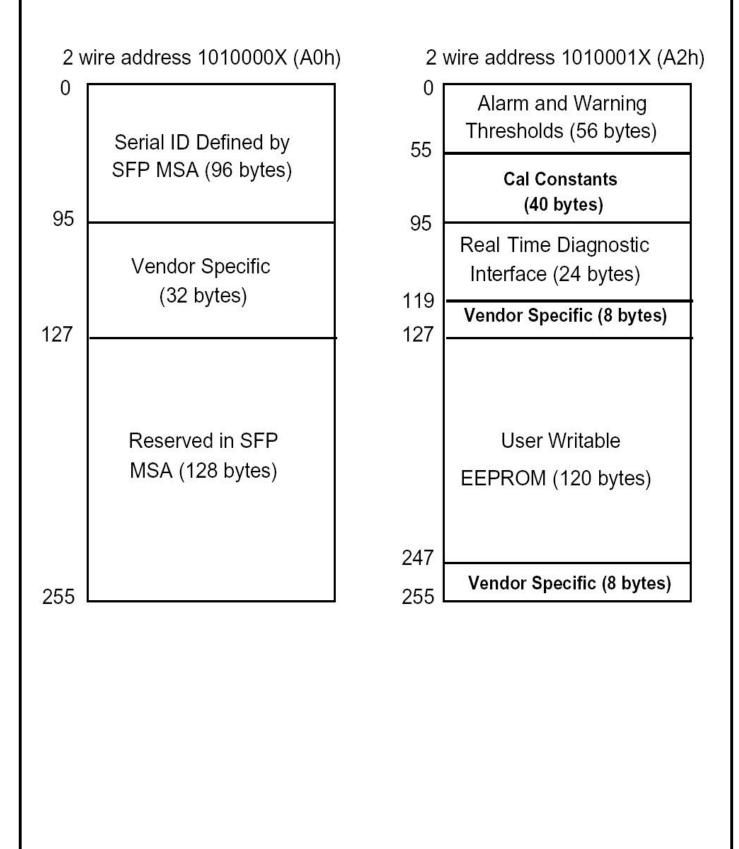
### **Digital Diagnostic Memory Map**

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including

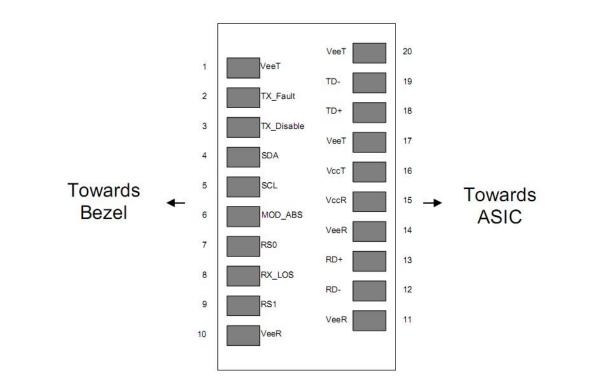
received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.



6

## **Pin Descriptions**



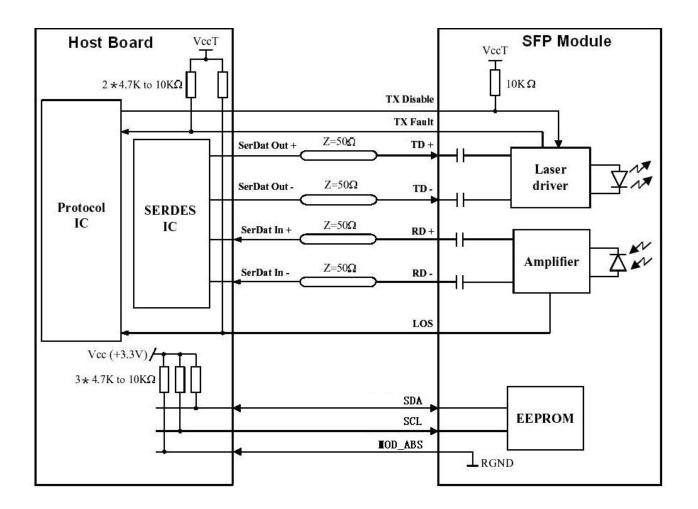
Pin	Signal Name	Description	Plug Seq.	Notes
1	V <sub>EET</sub>	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	SDA	SDA Serial Data Signal	3	
5	SCL	SCL Serial Clock Signal	3	
6	MOD_ABS	Module Absent. Grounded within the module	3	
7	RS0	Not Connected	3	
8	LOS	Loss of Signal	3	Note 3
9	RS1	Not Connected	3	
10	V <sub>EER</sub>	Receiver ground	1	
11	V <sub>EER</sub>	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 4
13	RD+	Received Data Out	3	Note 4
14	VEER	Receiver ground	1	
15	V <sub>CCR</sub>	Receiver Power Supply	2	
16	Vсст	Transmitter Power Supply	2	
17	VEET	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 5
19	TD-	Inv. Transmit Data In	3	Note 5
20	VEET	Transmitter Ground	1	

Notes:

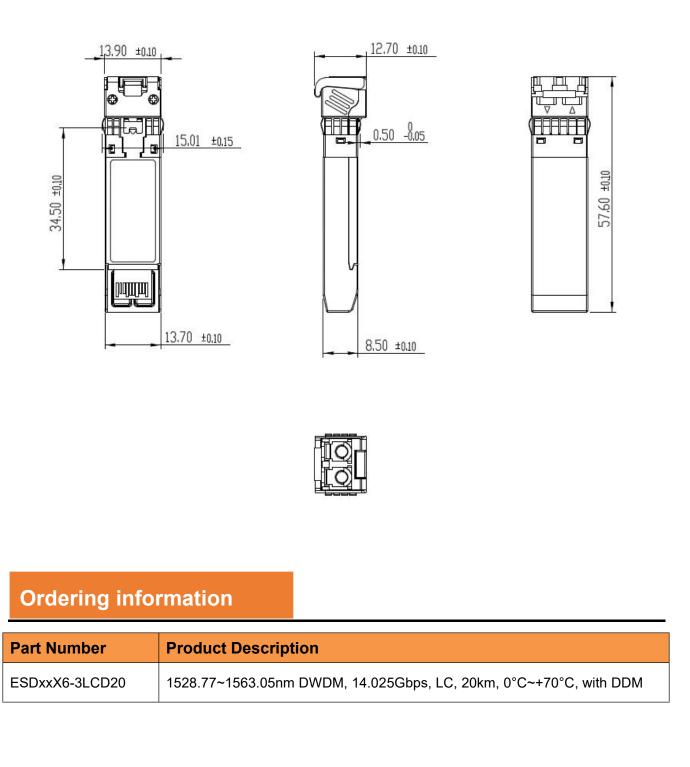
Plug Seq.: Pin engagement sequence during hot plugging.

- TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
- 3) LOS is open collector output. Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.
- 4) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- 5) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

### **Recommended Interface Circuit**



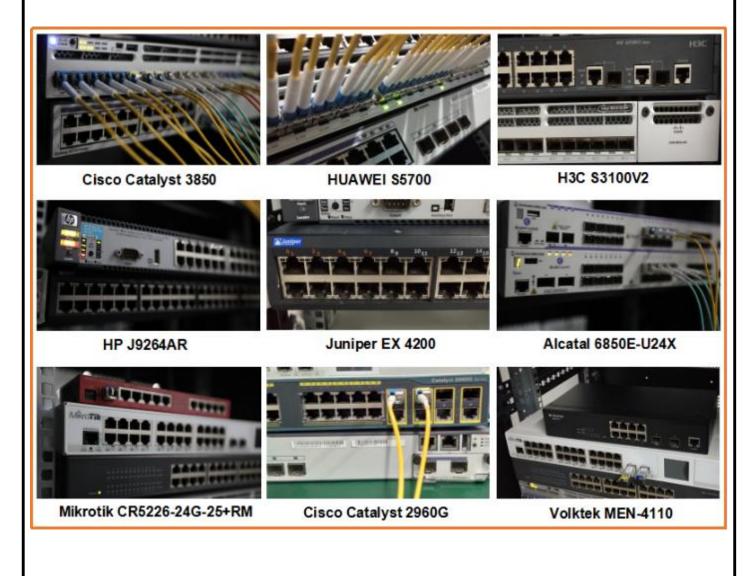
### **Mechanical Dimensions**



### **Compatibility Test**

In order to ensure the product compatibility, our products will be tested on the switch before shipment. Our modules can compatible with many mainstream brand switches, such as Cisco, Juniper, Extreme, Brocade, IBM, H3C, HP, Huawei, D-Link, Mikrotik, ZTE, TP-Link...

Our test equipment: VOLKTEK MEN-4110, HP 2530-8G, CRS226-24G-25+RM, Catalyst 2960G Series, Catalyst 3850 XS 10G SFP+, Catalyst 3750-E Series, HUAWEI S5700Series, H3C S3100V2 Series, Juniper-EX4200, etc.



### **Product Production Process**

# **Quality Assurance**

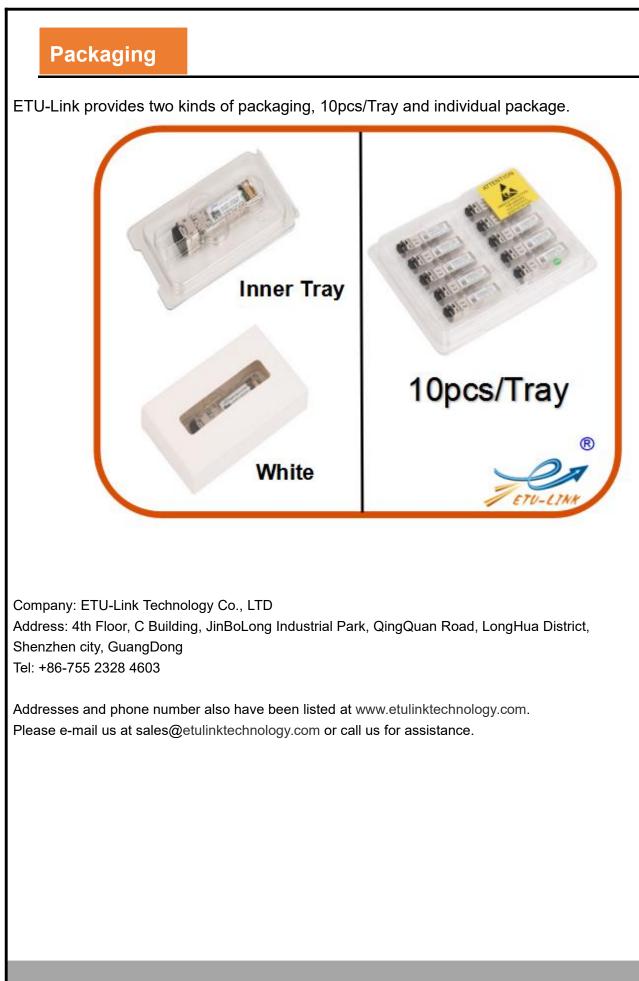
Continuous introduction of new equipment, produced by strict standards, strict quality inspection, to guarantee the high quality standard of each product.



**Product Initial Test** 

Switch Testing

**Product Final Test** 



Fiber Optic Transceivers Copyright 2011—2017 etulinktechnology.com All Rights Reserved